XA-9611 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroshi OKABE et al.

Appln. No.: 10/044,910 Group Art Unit: 2827

Filed: January 15, 2002 Examiner: T. Dinh

For: ELECTRONIC CIRCUIT EQUIPMENT USING MULTILAYER CIRCUIT

BOARD

Allowed: January 20, 2004

Confirmation No.: 4961

TRANSMITTAL OF CORRECTED FORMAL DRAWING

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Attached hereto is a copy of the corrected formal drawing which accompanied the Amendment filed October 30, 2003.

Respectfully submitted,

NHS: 1mb

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March 17, 2004

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